

Fig.1.

	WORD ADDRESS	BYTE ADDRESS	INSTRUCTION NUMBER
101	X	0 0 0 0 0	0
	X	0 0 0 0 1	1
	X	0 0 0 1 0	2
	X	0 0 0 1 1	3
	X	0 0 1 0 0	4
102	13 cj	0 0 1 0 1	5
	X	0 0 1 1 0	6
	0 cj	0 0 1 1 1	7
	X	0 1 0 0 0	8
	13 cj	0 1 0 0 1	9
103	X	0 1 0 1 0	10
	X	0 1 0 1 1	11
	16 j	0 1 1 0 0	12
	X	0 1 1 0 1	13
	2 cj	0 1 1 1 0	14
104	X	0 1 1 1 1	15
	X	1 0 0 0 0	16
	X	1 0 0 0 1	17
	X	1 0 0 1 0	18
	X	1 0 0 1 1	19
105			

Fig.2.

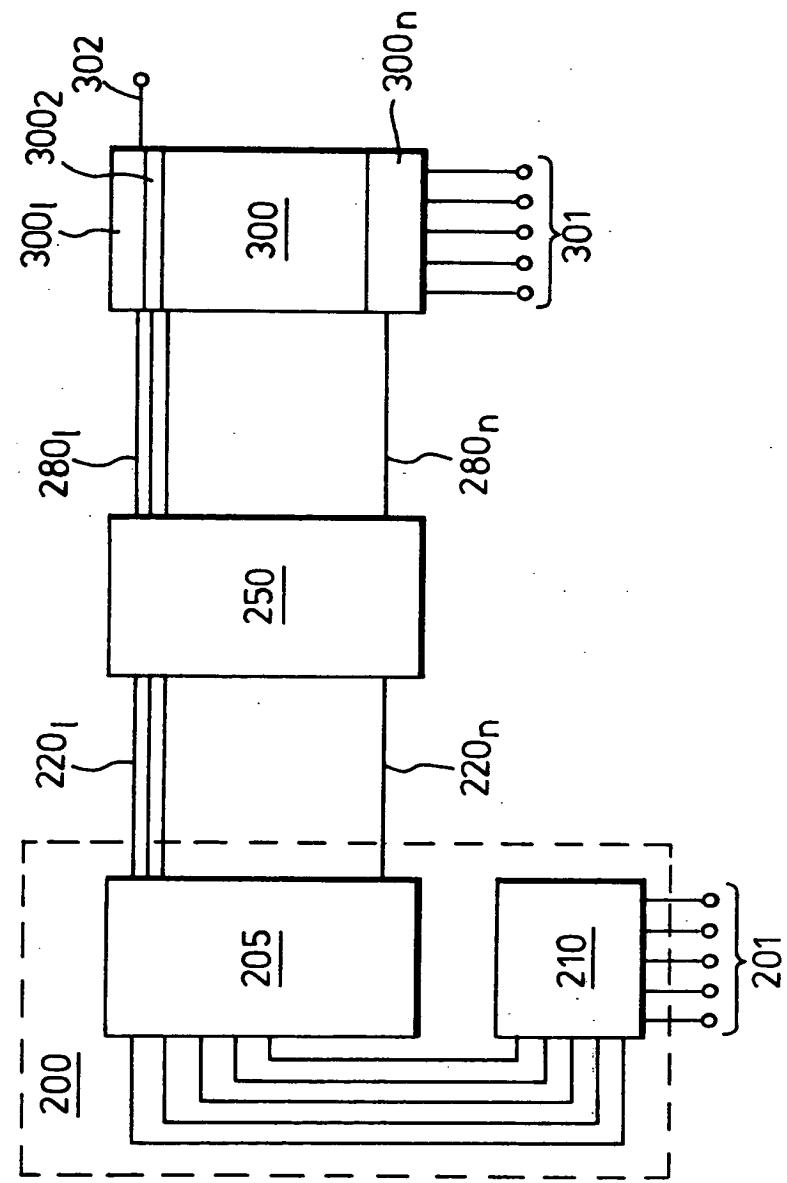


Fig.3.

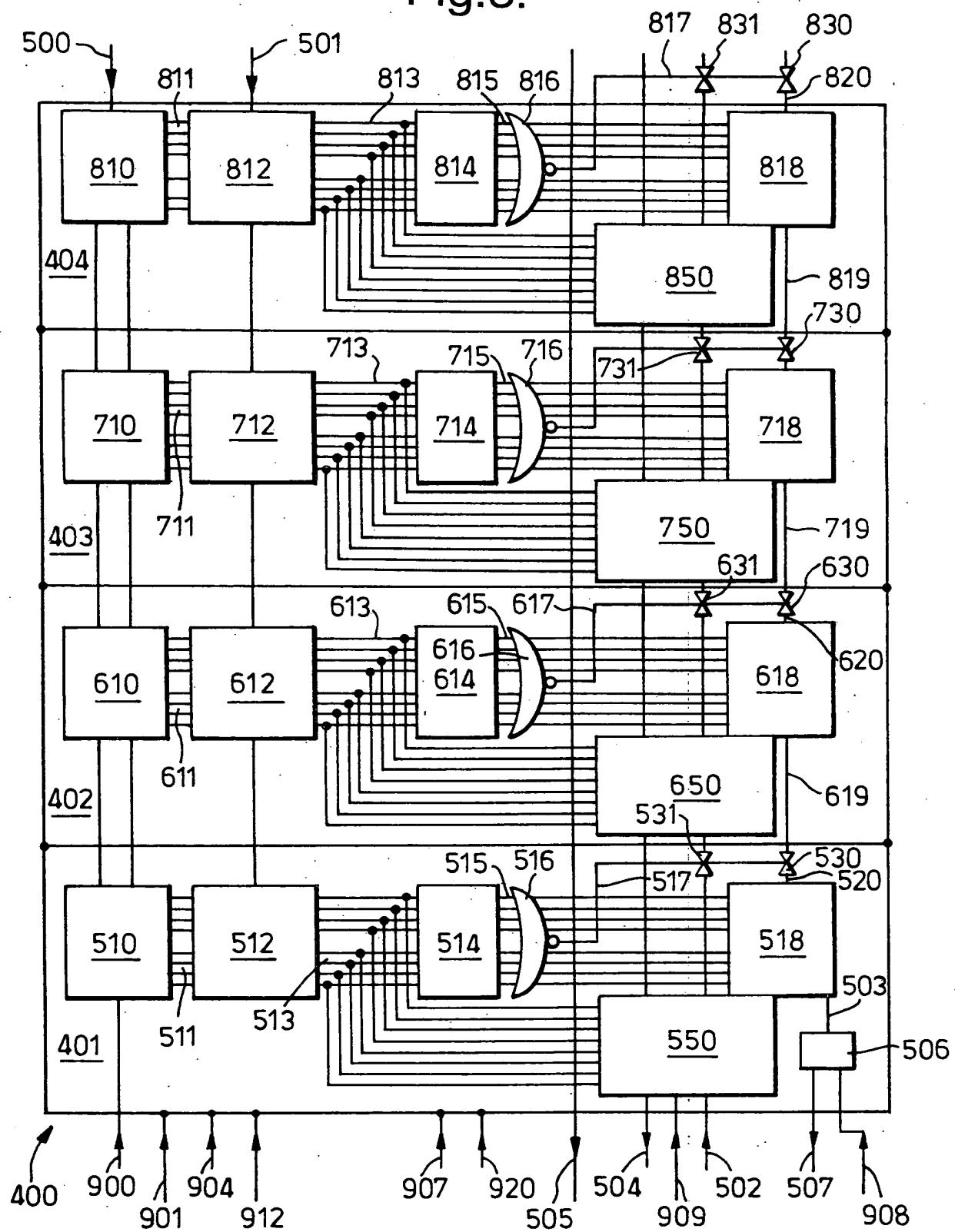


Fig.4.

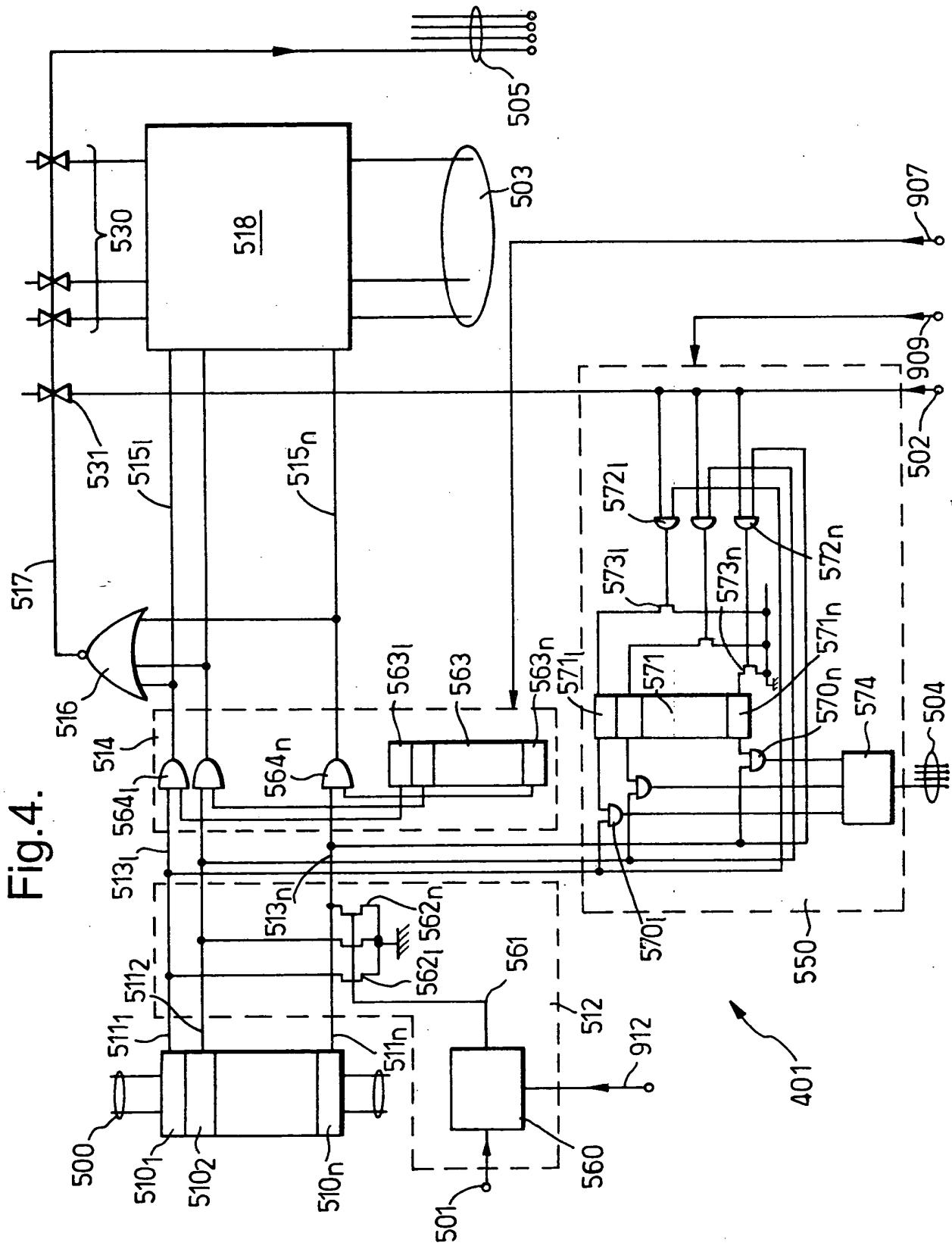
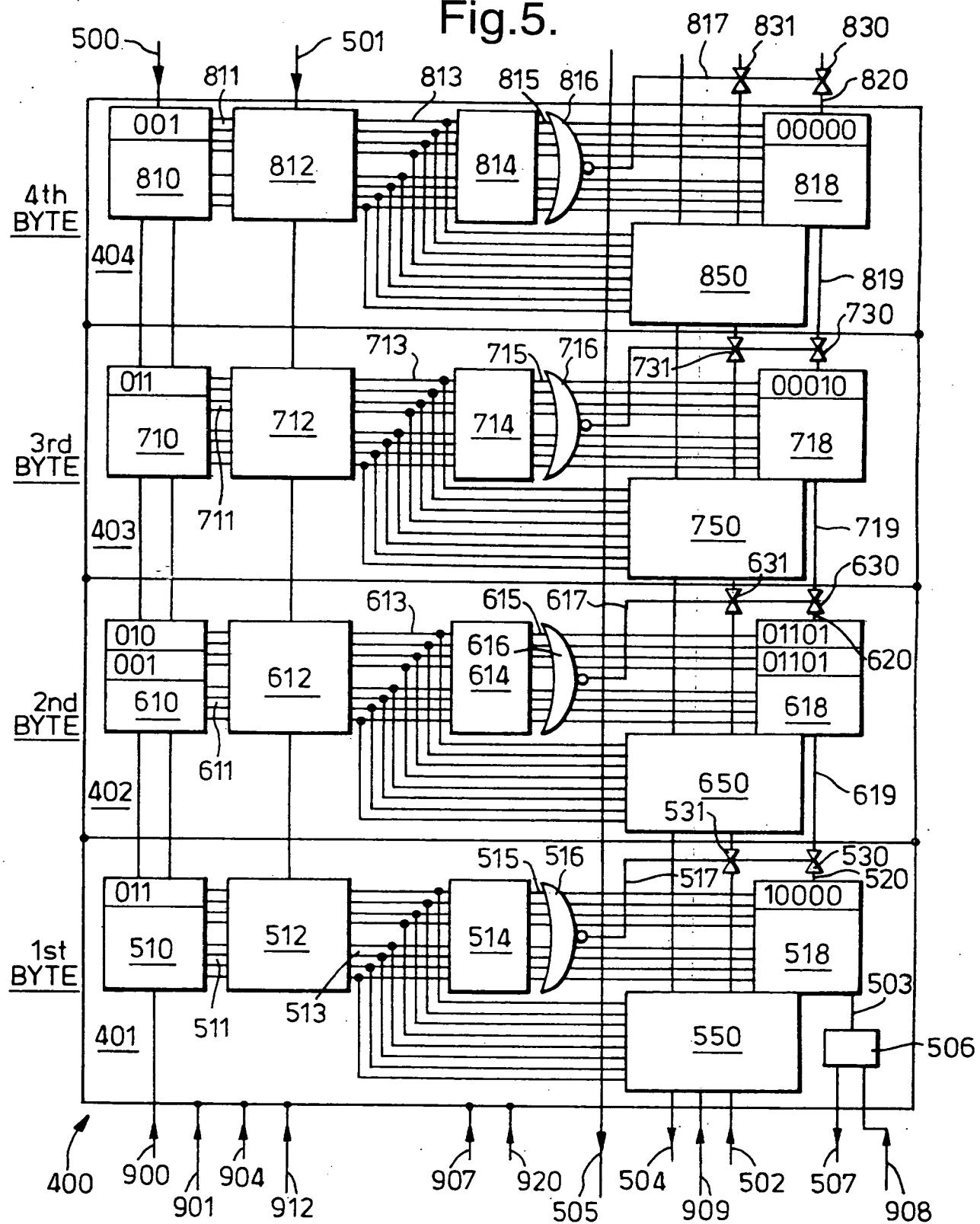


Fig.5.



CYCLE
PREDICTION
INTERIM UPDATE
RESOLUTION
RESOLVED PREDICTION

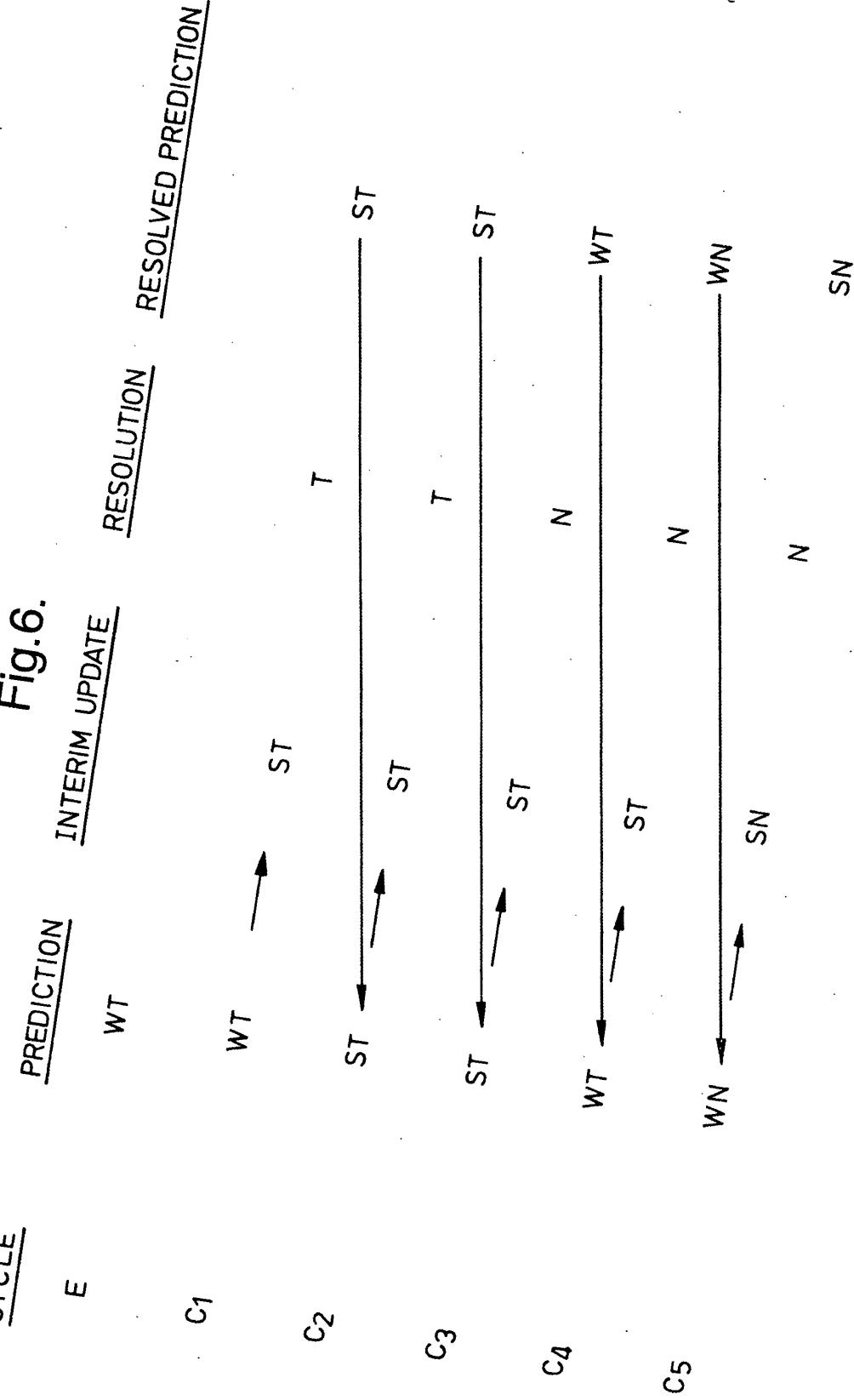


Fig. 7.

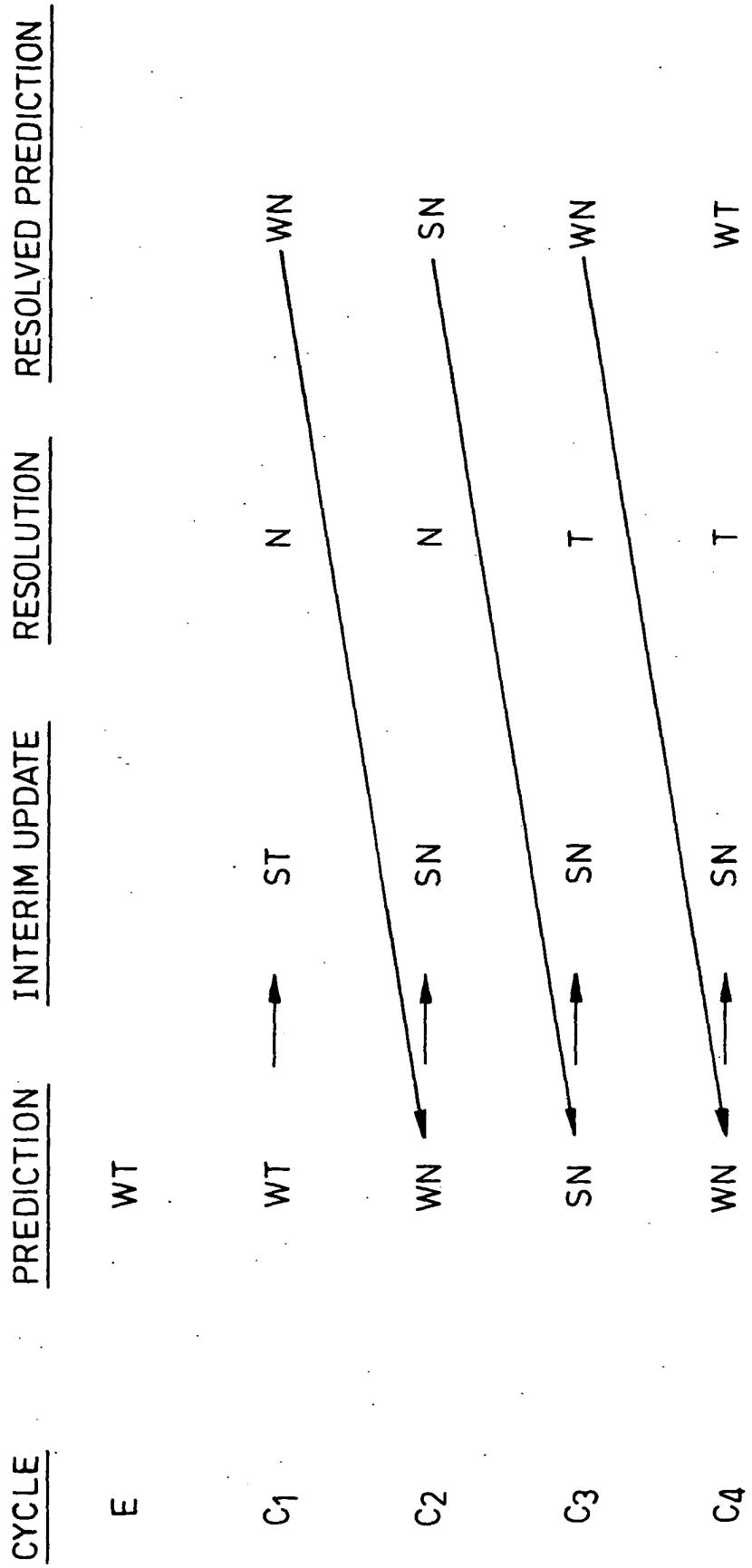


Fig. 8

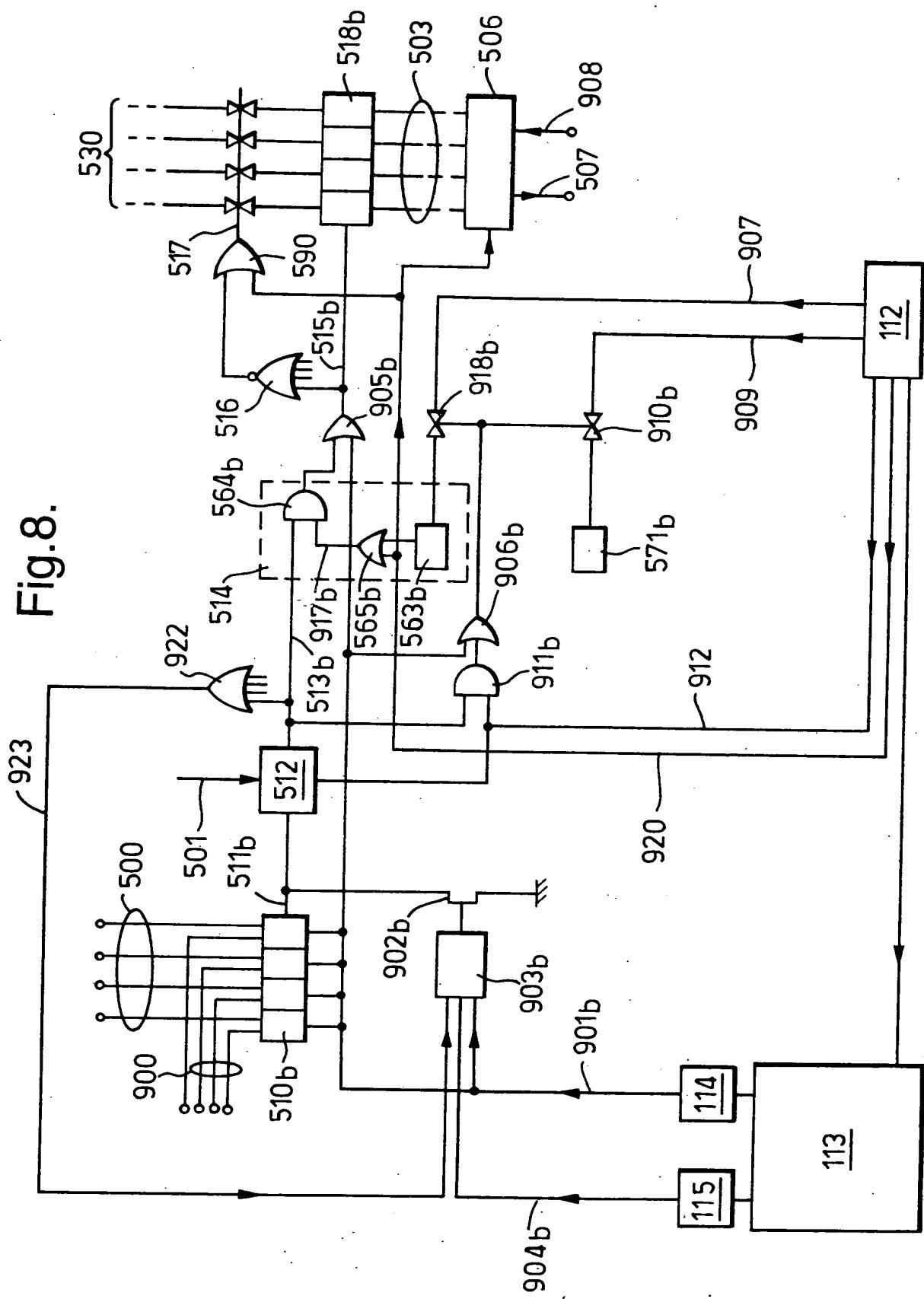


Fig.9.

